



# INFORMATION DISCLOSURE STATEMENT BY APPLICANT

**(use as many sheets as necessary)**

Sheet	1	of	2
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**Complete if Known**

Application Number	10/010,994
Filing Date	November 12, 2001
First Named Inventor	Randy B. Osborne
Art Unit	2176
Examiner Name	Unknown
Attorney Docket Number	42390P12472

## U.S. PATENT DOCUMENTS

[illegible]

## FOREIGN PATENT DOCUMENTS

[illegible]

Examiner Signature	<i>midys Inoa</i>	Date Considered	2	23	04
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Based on PTO/SB/08A (08-03) as modified by Blakely, Solokoff, Taylor & Zafman (wtr) 08/11/2003.

Send To: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450



Substitute for form 1001 <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>				<b>Complete if Known</b>	
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Sheet	2	of	2		

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
MI	F	IYER R, BHUYAN LN, NANDA A: "Using Switch Directories to Speed Up Cache-to-Cache Transfers in CC-NUMA Multiprocessors, CA Conference Article, May 5, 2000, XP002248955, Cancun Mexico.	
MI	G	LI-SHIUAN PEH ET AL: "Flow Control and Micro-Architectural Mechanisms for Extending the Performance of Interconnection Networks; 17-36, 105-138" XP002248956, Stanford University, retrieved from www-cs-students.stanford.edu on 7/24/2003.	

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